

Integrated circuit for analog system.

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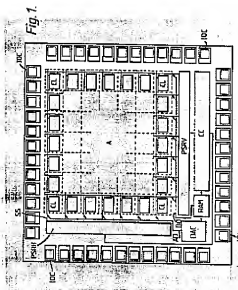
Cited documents:

FR2417132
US3243582

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Abstract of EP0450863

This invention relates to semiconductor integrated circuits which find utility in analogue systems. An integrated circuit of the invention comprises an array (A) of configurable analogue cells (CL) each of which is capable of interconnection with other cells in the array by means of an interconnection network (HB, VB). Each of the cells can be selectively and individually selected by means of select signals (SS) and (DD) emanating from shift registers (DSRH, PSRV). When selected the cell (CL) may be configured with configuration data (DD, AD) which both sets the cell to take up a particular electrical configuration by means of digital data (DD) and sets various programmable resistors and capacitors (P/res, P/cap). In the cells to particular values by means of analogue signals (AD). The configuration data is held in a random access memory RAM. Some of this data is converted to analogue form (AD) by means of a digital-to-analogue converter (DAC). The circuit is controlled by a central control (CC) and is capable of being configured to implement particular analogue functional applications from a plurality of possible analogue functional applications. Provision in the circuitry is made to automatically compensate for manufacturing component tolerances.



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